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(11)

EP 0 768 562 A2

(12)

## EUROPEAN PATENT APPLICATION

(43) Date of publication:  
16.04.1997 Bulletin 1997/16

(51) Int Cl.<sup>6</sup>: G02F 1/139

(21) Application number: 96307391.1

(22) Date of filing: 10.10.1996

(84) Designated Contracting States:  
DE FR GB

(30) Priority: 13.10.1995 GB 9521043

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### (54) Liquid crystal device and method of making a liquid crystal device

(57) A surface mode liquid crystal device is made by forming a cell (1) containing a mixture of a liquid crystal (7) and a pre-polymer. An electric field is applied across the cell (1) to arrange the liquid crystal (7) in a predetermined surface mode state, such as the V state in the case of a pi-cell configuration. The liquid crystal (7) is

held in this state by the field while the pre-polymer is polymerised or cross-linked, for instance by ultraviolet irradiation. The electric field is then removed. The polymer stabilises the predetermined surface mode state, for instance preventing relaxation of the V state to the twist or splay state in the pi-cell configuration.

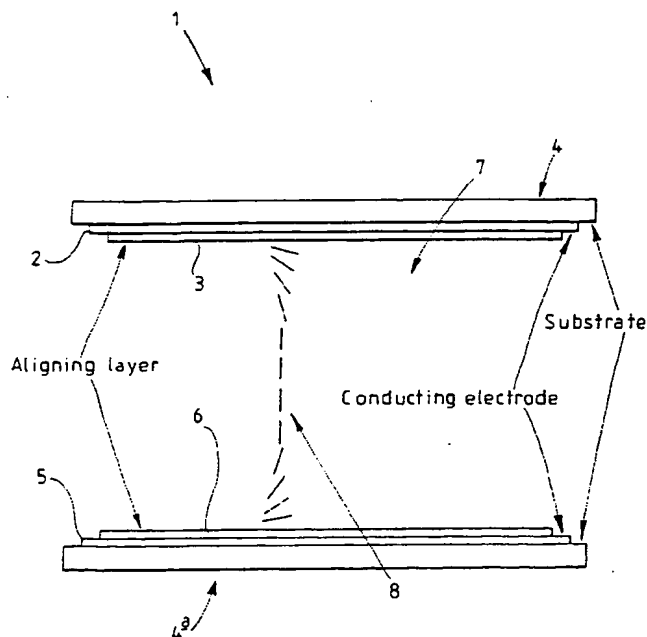


FIG. 2

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## Description

The present invention relates to a liquid crystal device and to a method of making a liquid crystal device. Such devices may be used to provide fast switching liquid crystal displays having applications in television, computer displays, and three dimensional displays. Such devices may also be used in optical systems requiring fast polarisation switches, spatial light modulators, and optical shutters for instance for optical communications.

The term "surface mode liquid crystal device" as used herein means a liquid crystal device in which optical change caused by varying the field across the liquid crystal occurs primarily in the surface layers of the liquid crystal. One example of such a device is known as a pi-cell, in which the liquid crystal is disposed between alignment layers which create parallel alignment. This may be achieved by providing polyimide alignment layers which are rubbed in the same direction. Another example of such a device is known as a Fredericks cell, in which the liquid crystal is disposed between alignment layers which create antiparallel alignment. For instance, the display may have polyimide alignment layers rubbed in opposite directions. Other types of surface mode liquid crystal devices are possible, for instance employing parallel alignment but operating in a splay state as described hereinafter.

Surface mode liquid crystal devices (LCDs) are disclosed in *Mol Cryst. Liq. Cryst.*, 1972, 19, 123-131 "Deformation of Nematic Liquid Crystals in an Electric Field", *Sov. J. Quant. Electron.*, 1973, 3, 78-9, "Electro-Optic Switching in Oriented Liquid Crystal Films", and US 4 385 806. LCDs of the pi-cell type are disclosed in *Mol Cryst. Liq. Cryst.*, 1984, 113, 329-339, "The Pi-cell: A Fast Liquid Crystal Optical Switching Device.", US 4 635 051 and GB 2 276 730. A typical known pi-cell structure comprises a liquid crystal layer disposed between parallel-rubbed polyimide alignment layers and provided with suitable addressing electrodes. A retarder whose optic axis is perpendicular to the alignment direction of the liquid crystal layer may be disposed adjacent the layer so as to compensate for the retardation of the layer and thus lower the required operating voltage range by allowing zero retardation to be achieved at a finite voltage. This assembly is sandwiched between crossed linear polarisers whose polarisation vectors are at 45 degrees to the optic axes of the liquid crystal layer and the retarder. A display using this structure provides fast switching times between on and off states, for instance of the order of a millisecond or less.

In the absence of an electric field across the liquid crystal layer, the liquid crystal is in a splay mode, as described in more detail hereinafter. For the display to operate in the pi-cell mode, the liquid crystal has to be transformed in to the V state, as also described hereinafter in more detail, by the application of a suitable electric field. However, when a suitable field is initially ap-

plied, it takes several seconds for the display to change to the V state. Transformation from the splay state to the V state occurs when the voltage across the cell electrodes exceeds a critical voltage and the drive voltage for the cell must therefore always exceed this critical voltage during operation of the display. If the drive voltage falls below a critical value, the liquid crystal relaxes to a twisted state, in which there is a 180 degree twist of the liquid crystal between the alignment surfaces.

The Conference record of the International Display Research Conference, 1991, 49-52 "Cholesteric Liquid Crystal/Polymer Gel Dispersion Bistable at Zero Field" discloses the use of a polymer to stabilise cholesteric features of a LCD. The display is formed by adding a small quantity of monomer to a cholesteric liquid crystal. Polymerisation of the monomer alters the switching properties of the display such that two quasi-stable states are formed. These states can exist in the absence of an applied field across the liquid crystal and can be addressed by appropriate voltage pulses across the liquid crystal. This paper alleges that the liquid crystal/polymer gel dispersion is responsible for creating these quasi-stable states.

The Conference record of the International Display Research Conference, 1994, 480-483, "Polymer Stabilised SBE Devices" discloses a technique for inducing a bulk pretilt of the liquid crystal in super-twisted birefringent effect (SBE) devices also known as super-twisted nematic (STN) devices. A monomer is added to the liquid crystal and is polymerised while applying a voltage across the liquid crystal cell. The effect of the polymer is to eliminate stripe formation from SBE or STN displays by inducing a bulk pretilt of the liquid crystal.

GB 2 286 896 discloses LCD's in which isolated pockets or regions of liquid crystal are formed by polymerising or cross-linking relatively high concentrations of pre-polymers.

According to a first aspect of the invention, there is provided a method as defined in the appended Claim 1.

According to a second aspect of the invention, there is provided a device as defined in the appended Claim 12.

According to a third aspect of the invention, there is provided a device as defined in the appended Claim 13.

Preferred embodiments of the invention are defined in the other appended claims.

It is thus possible to provide a surface mode liquid crystal device which is stabilised in a predetermined surface mode state. For instance, in the case of a pi-cell stabilised in the V state, the V state is retained in the absence of an applied field across the liquid crystal. The V state is retained in the absence of a field so that no time is required to reform the V state after the device has been switched off. Further, such a device may be used with a drive scheme in which one drive voltage is zero. The wide viewing angle of the pi-cell mode is unaffected by this process. The effect on switching speed or response time of the device is relatively small so that

the device retains the advantages of pi-cells while overcoming the disadvantages described hereinbefore.

The invention will be further described, by way of example, with reference to the accompanying drawings, in which:

Figure 1 is a schematic diagram illustrating a pi-cell LCD in a splay state;

Figure 2 is a schematic diagram illustrating the cell of Figure 1 in the V state; and

Figure 3 is a schematic diagram illustrating a LCD incorporating the cell of Figure 1.

Figure 1 shows a liquid crystal device comprising a cell 1 having a first substrate 4 on which are deposited a conducting electrode 2 and an aligning layer 3. A second substrate 4 a similarly carries a conducting electrode 5 and an aligning layer 6. A liquid crystal 7 forms a layer between the aligning layers 3 and 6. The aligning layers 3 and 6 comprise polyimide layers which have been subjected to rubbing substantially parallel and in the same direction so as to provide parallel (as opposed to anti-parallel) alignment. However, any suitable technique may be used for achieving the desired alignment. Such techniques include oblique evaporation (for instance of silicon dioxide), illumination of certain materials by polarised light, and surface relief profile, for instance in the form of diffraction gratings.

Figure 1 illustrates the splay state of the cell which is the state adopted by the liquid crystal 7 in the absence of an electric field, in particular in the absence of a potential difference between the electrodes 2 and 5. The directors of the liquid crystal molecules are illustrated by the short lines at 8. At the surfaces of the aligning layers 3 and 6, the directors are substantially parallel to each other (in the plane of the cell) and point in the same direction. The liquid crystal directors at the aligning layers 3 and 6 have a pretilt of the order of between 1 and 10 degrees with respect to the surfaces of the aligning layers 3 and 6. The directors of the liquid crystal throughout the cell are parallel or nearly parallel to each other as illustrated.

In order to establish pi-cell operation, the liquid crystal has to be changed to the V state as shown in Figure 2. An alternating voltage is applied between the electrodes 2 and 5 with an RMS amplitude greater than a threshold value, which is of the order of 1.5 to 2 volts for the liquid crystal material E7, so that the liquid crystal changes from the splay state to the V state illustrated by the liquid crystal molecule directors in Figure 2. As described hereinbefore, it takes some time for the V state to be established, typically of the order of seconds or even minutes. Once the V state has been established, in known cells, the drive voltage must not fall below a predetermined value in order to maintain pi-cell operation in the V state. During normal operation in the pi-cell

mode, the directors in the liquid crystal layers adjacent the aligning layers 3 and 6 change direction in accordance with the amplitude of the voltage applied between the electrodes 2 and 5. This causes the retardation of the cell to vary and this effect may be used to provide a display as described hereinafter.

When the drive voltage applied to the electrodes 2 and 5 is reduced below a critical voltage (typically about 1.2 to 1.6 volts for the liquid crystal material E7), the liquid crystal 7 relaxes from the V state to the twist state. If the drive voltage remains below this voltage for any substantial time, the twist state gradually relaxes further to the splay state, which typically takes from less than a second to a few minutes. If a suitable drive voltage is re-applied to the cell when in the twist state, the liquid crystal 7 may be returned directly to the V state, although the response times for the transitions to and from the twist state are very much slower than typical response times for switching within the V state. However, if any domains of splay have formed in the twist regions, the initial slow transition from the splay state to the V state must be repeated to remove these domains.

In order to stabilise the V state, a cell of the type shown in Figures 1 and 2 is formed. However, the liquid crystal 7 includes a small quantity of a pre-polymer which may, for instance, be liquid crystalline or non-mesogenic. The pre-polymer may, for instance, comprise a monomer, an oligomer, a polymer capable of further cross-linking or a mixture of such compounds. A drive voltage of sufficient amplitude to change the liquid crystal to the V state is applied between the electrodes 2 and 5 and, once the V state has been established, the pre-polymer is cross-linked or polymerised, for instance by ultraviolet irradiation, while maintaining the drive voltage between the electrodes 2 and 5. When polymerisation or cross-linking is complete, the drive voltage may be removed. However, instead of relaxing to the splay state, the liquid crystal 7 is stabilised in the V state and remains in this state irrespective of whether a drive voltage is applied between the electrodes 2 and 5. Accordingly, the cell functions immediately upon applying a drive voltage, which voltage may have zero amplitude for one state of the pi-cell. Thus, the inconvenience of having to wait for the V state to form when a drive voltage is applied is eliminated. Further, during operation, the inconvenience of having to maintain a drive voltage greater than a predetermined amplitude is also eliminated, which allows simpler and more convenient drive arrangements for the cell.

Figure 3 illustrates a possible arrangement of a liquid crystal display using the pi-cell 1 illustrated in Figures 1 and 2 but including the stabilising polymer as described hereinbefore. A linear polariser 10 having its polarisation vector at 45 degrees to the optic axis of the cell 1 is disposed on one side of the cell. On the other side of the cell are arranged a positive retarder 11, a negative retarder 12, and a linear polariser 13 whose polarisation vector is perpendicular to that of the polar-

iser 10. The optic axis of the retarder 11 is perpendicular to the optic axis of the liquid crystal of the cell 1. The optic axis of the retarder 12 is perpendicular to the surfaces of the cell 1.

At a predetermined wavelength of visible light, the cell 1 provides a retardation which is variable between upper and lower values for drive voltage amplitudes between zero and an upper value, respectively. At the upper value, the retardation of the cell 1 has a finite value  $\delta$  so that, for the predetermined wavelength, the retardation varies between  $\lambda/2 + \delta$  and  $\delta$ . The retarder 11 provides a retardation of  $\delta$  so that the effect of the cell 1 and the retarder 11 is to provide a retardation at the predetermined wavelength of between  $\lambda/2$  and zero. The negative retarder 12 is provided to improve the viewing angle performance of the display.

Switching between the two states of the cell 1 in combination with the polarisers 10 and 13 causes the display to switch between transparent and opaque modes with respect to the predetermined wavelength. It is thus possible to provide a monochrome display which may be pixellated and provided with colour filtering to provide a colour display. As an alternative to colour filtering, because the response time of the display is so fast, colour may be generated by time-sequential techniques. The switching speed of the display is rapid and is similar to the switching speed of known pi-cell displays.

The use of positive retarders is described in EP 0 616 240 and the use of negative retarders is described in "Proceedings of the 13th International Display Research Conference", 1993, pp 149.

#### Example 1.

A two micrometer parallel aligned cell of the type illustrated in Figures 1 and 2 was filled with a mixture of E7 and a small quantity of a reactive mesogen (liquid crystal) RM82, which is a diacrylate monomer material which polymerises to form a cross-linked network. Both materials are available from Merck. The cell was switched into the V state and held at a voltage of 10 volts while the reactive mesogen was polymerised by exposure to ultraviolet light at a wavelength of 365 nanometers. Concentrations from 1.5% to 5% of the mixture by weight of the reactive mesogen were tested and it was found that, on removal of the electric field after polymerisation, the cell remained in the V state for concentrations above approximately 2%. Switching the cell between two voltages corresponding to retardation values of approximately  $\lambda/2$  and 0 gave response times of approximately 1.5 milliseconds for the rise time and between 2 and 4 milliseconds for the decay time. Although these response times are longer than for a conventional pi-cell without the polymer, which are of the order of 1 and 2 milliseconds, respectively, they are comparable and sufficiently fast for most applications while having the advantage of the stabilised V state which extends

the possible applications for the polymer stabilised pi-cell beyond those for the conventional pi-cell.

#### Example 2.

Example 1 was repeated but with the voltage applied to the cell reduced to just above the voltage for the transition from the V state to the twist state, as mentioned hereinbefore. The transition voltage was approximately 1.8 volts. The effect of this was to stabilise the V state with a less "homeotropic-like" director profile than in Example 1 so as to obtain a greater change in retardation for a given cell thickness and between given finite voltages. It was found that, for concentrations of the polymer of less than 2% by weight of the mixture, only the splay state existed after removal of the voltage. However, above concentrations of 3%, the cell remained stabilised in the V state after removal of the voltage. For intermediate concentrations, a mixture of states was obtained depending on the variation in cell thickness across the sample. The response times were similar to those for Example 1.

#### Example 3.

Example 1 was repeated for a 2% concentration of RM82 but using parallel aligned cells of different pretilt angles. It was found that increasing the pretilt angle favoured stabilisation of the V state. In a cell with a pretilt angle of 1 degree, the liquid crystal relaxed to the splay state on removal of the voltage. For a pretilt angle of 10 degrees, the liquid crystal remained stabilised in the V state at zero volts.

#### Example 4.

A pi-cell of thickness 12 micrometers was constructed and stabilised in the V state using a concentration of 2.85% of RM82 cured with an applied voltage of 1.8 volts. The cell was combined with a retarder, for instance as disclosed in EP 0 616 240 and illustrated in Figure 3, to allow the effective operating voltage to be reduced. A retardation switching range of between zero and  $\lambda/2$  was obtained for drive voltages between 10 and 2 volts, respectively. It was thus possible to provide a fast switching black/white mode liquid crystal display which was stabilised in the V state.

#### Example 5.

Example 1 was repeated but with the RM82 replaced by PN393 (available from Merck), which is non-mesogenic pre-polymer material comprising a mixture of monomeric and oligomeric compounds. A concentration of 2.5% by weight of the mixture of PN393 was used. A cell was produced in which the liquid crystal remained stabilised in the V state after removal of the electric field.

**Example 6.**

In preparing the examples described hereinbefore, some cells were stabilised in a state different from the V state upon removal of the electric field despite polymerisation having taken place in the V state. This occurred for relatively low concentrations of polymer and for relatively low electric fields, for instance 2% of RM82 cured at 1.8 volts. The stabilised state thus produced is thought to be equivalent to the splay state. However, no tilt walls were observed when a voltage was applied, indicating that the state was stabilised and that the switching process was slightly different from the non-polymer splay state. When a voltage was reapplied to the cell, the liquid crystal could not be switched into the V state. The cell had a greater retardation range than for a pi-cell operating in the V state but the decay response time was longer than for the V state, in particular about 20 milliseconds compared with approximately 2 to 4 milliseconds for the stabilised V state cell. This type of mode may also be formed by ultraviolet photopolymerisation with no voltage applied to the cell.

Although there was a reasonably well defined lower limit for the concentration of polymer in the mixture, namely about 2% by weight for most applied voltages during curing, an upper limit is less easy to define. For relatively high concentrations of polymer, the mixture forms a polymer dispersed liquid crystal with pockets of the liquid crystal being distributed in the polymer. For intermediate concentrations, the polymer forms interfaces which create isolated regions of the liquid crystal. In order to stabilise the V state or any other desired surface mode state, the concentration of polymer should be insufficient for such isolated regions to form. However, the concentration can easily be determined by experiment.

**Claims**

1. A method of making a surface mode liquid crystal device, comprising forming a cell (1) containing a mixture (7) of a liquid crystal and a pre-polymer, arranging the liquid crystal in a predetermined surface mode state, polymerising or cross-linking the pre-polymer while maintaining the liquid crystal in the predetermined state.
2. A method as claimed in Claim 1, characterised in that the liquid crystal is arranged and maintained in the predetermined state by an electric field.
3. A method as claimed in Claim 1 or Claim 2, characterised in that the proportion of the pre-polymer in the mixture (7) is greater than two per cent by weight.
4. A method as claimed in any one of the preceding

claims, characterised in that the proportion of the pre-polymer in the mixture (7) is insufficient to form, when polymerised or cross-linked, isolated regions of the liquid crystal.

5. A method as claimed in any one of the preceding claims, characterised in that the pre-polymer is a reactive mesogen.
6. A method as claimed in any one of the preceding claims, characterised in that the pre-polymer contains a di-reactive compound.
7. A method as claimed in any one of the preceding claims, characterised in that the pre-polymer is polymerised or cross-linked by ultraviolet irradiation.
8. A method as claimed in any one of the preceding claims, characterised in that the cell (1) comprises alignment layers (3, 6) for forming parallel alignment of the liquid crystal.
9. A method as claimed in Claim 8 when dependent on Claim 2, characterised in that the electric field applied across the cell (1) is sufficient to maintain the liquid crystal in the V state.
10. A method as claimed in Claim 8 when dependent on Claim 2, characterised in that the electric field applied across the cell (1) is insufficient to maintain the liquid crystal in the V state.
11. A method as claimed in any one of Claims 1 to 7, characterised in that the cell comprises alignment layers for forming anti-parallel alignment of the liquid crystal.
12. A surface mode liquid crystal device made by a method as claimed in any one of the preceding claims.
13. A surface mode liquid crystal device characterised by comprising a cell (1) containing a mixture (7) of a liquid crystal in a predetermined surface mode state and a polymer for stabilising the predetermined surface mode state.
14. A device as claimed in Claim 13, characterised in that the proportion of the polymer in the mixture (7) is greater than two per cent by weight.
15. A device as claimed in Claim 13 or 14, characterised in that the cell (1) comprises alignment layers (3, 6) forming parallel alignment of the liquid crystal.
16. A device as claimed in Claim 15, characterised in that the predetermined surface mode state is the V state.

17. A device as claimed in Claim 15, characterised in that the predetermined surface mode state is the splay state.
18. A device as claimed in Claim 15, characterised in that the predetermined surface mode state is the twist state. 5
19. A device as claimed in Claim 13 or 14, characterised in that the cell comprises alignment layers forming anti-parallel alignment of the liquid crystal. 10

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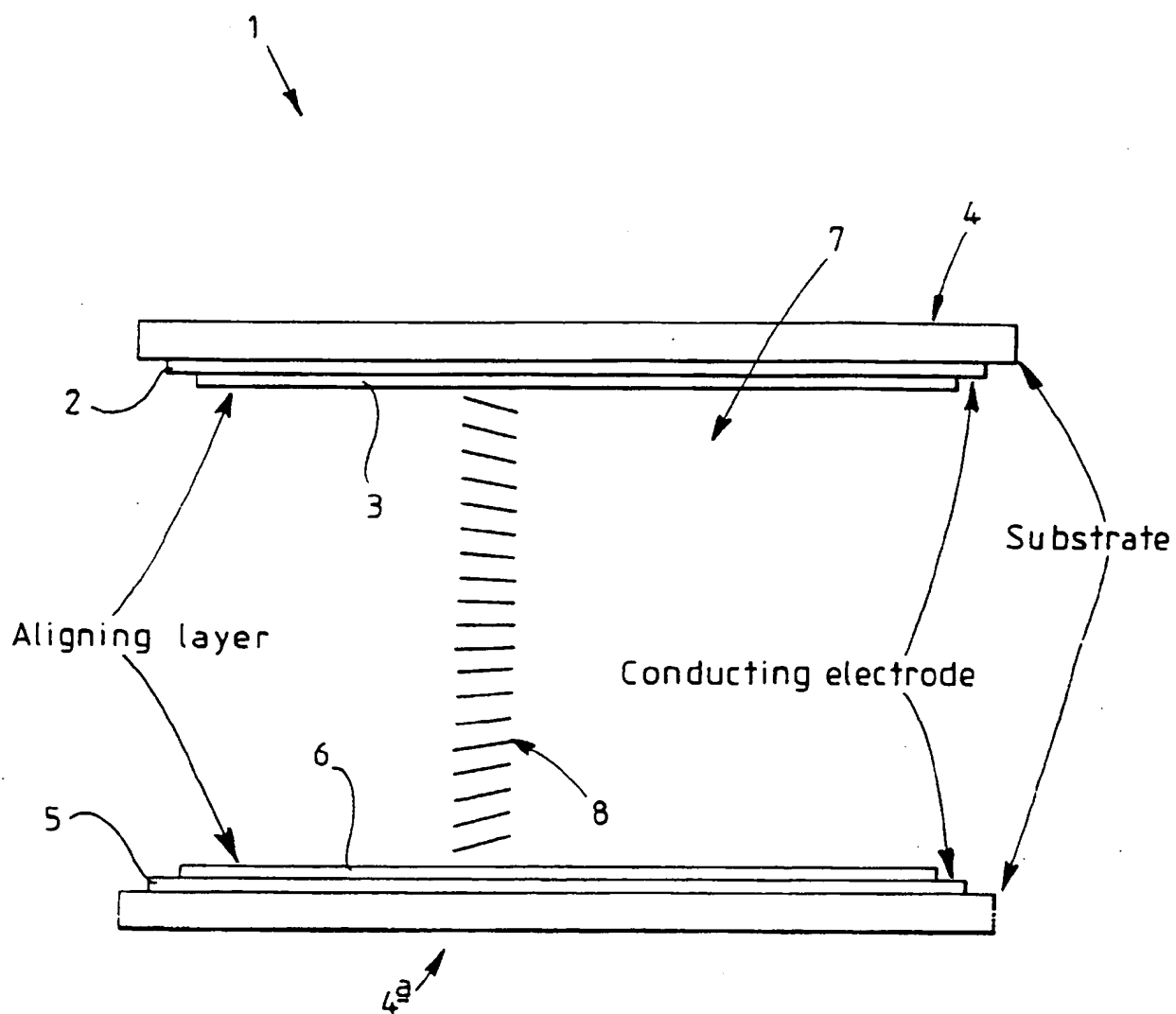


FIG 1

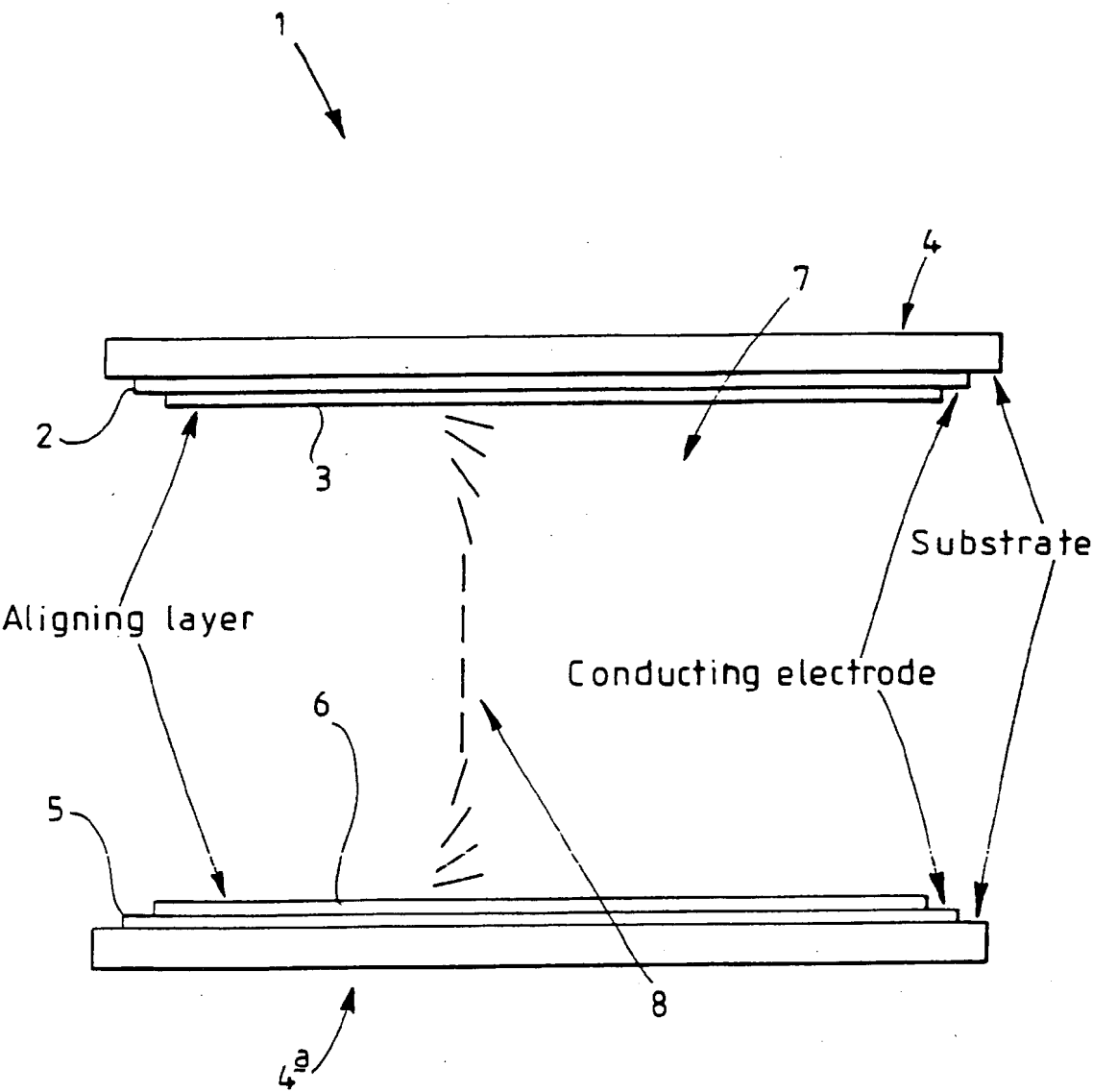


FIG 2



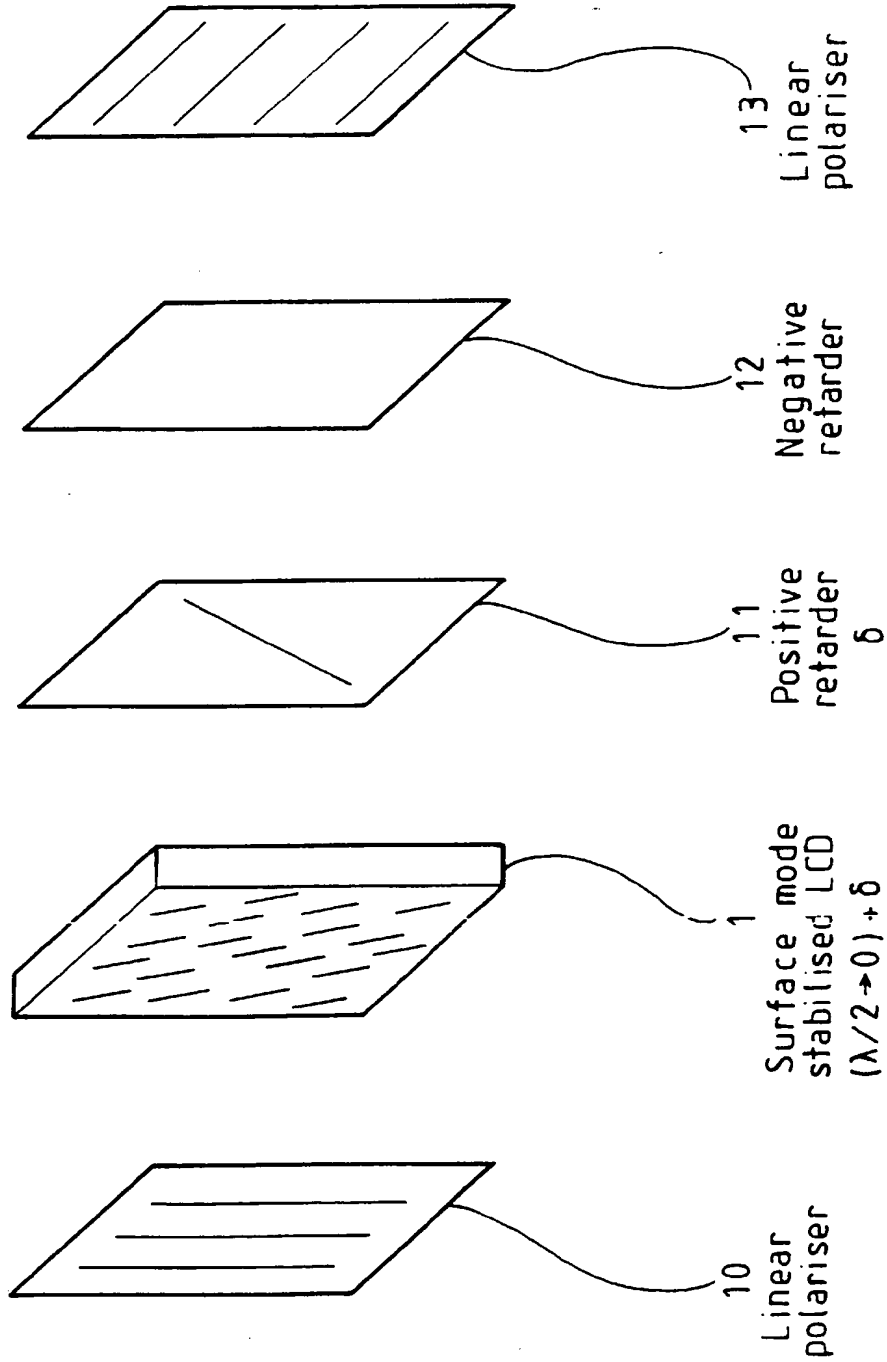
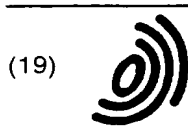


FIG 3





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(11) **EP 0 768 562 A3**

(12) **EUROPEAN PATENT APPLICATION**

(88) Date of publication A3:  
08.04.1998 Bulletin 1998/15

(51) Int Cl.<sup>6</sup>: **G02F 1/139**

(43) Date of publication A2:  
16.04.1997 Bulletin 1997/16

(21) Application number: **96307391.1**

(22) Date of filing: **10.10.1996**

(84) Designated Contracting States:  
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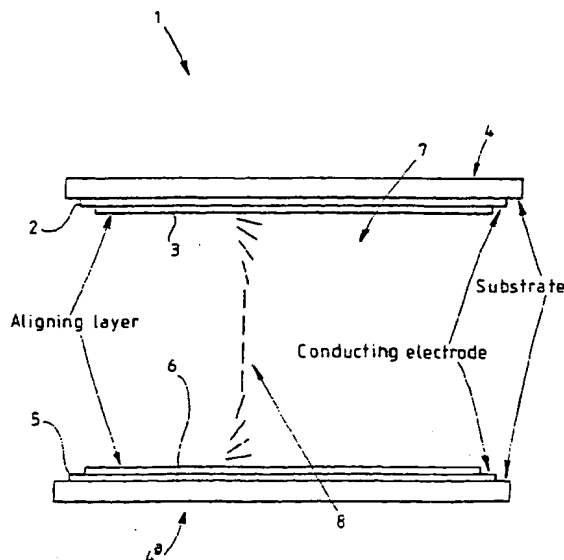
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**FIG. 2**

**EP 0 768 562 A3**



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# EUROPEAN SEARCH REPORT

Application Number  
EP 96 30 7391

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
Y, D	FREDLEY D S ET AL: "POLYMER STABILIZED SBE DEVICES" PROCEEDINGS OF THE INTERNATIONAL DISPLAY RESEARCH CONFERENCE JAPAN DISPLAY, 1994, pages 480-483, XP000061442 * the whole document *	1-19	G02F1/139
Y	BOS P J ET AL: "A LOW-THRESHOLD-VOLTAGE POLYMER NETWORK TN DEVICE" SID INTERNATIONAL SYMPOSIUM - DIGEST OF TECHNICAL PAPERS, SEATTLE, MAY 16 - 21, 1993, vol. 24 PART 1, 18 May 1993, SOCIETY FOR INFORMATION DISPLAY, pages 877-880, XP000541417 chapter: Conclusion	1-19	
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A	P.J. BOS: "The pi-Cell" MOLECULAR CRYSTALS AND LIQUID CRYSTALS (INC. NONLINEAR OPTICS ), vol. 113, 1984, READING GB, pages 329-339, XP002053315 * the whole document *	8-11, 15-18	
The present search report has been drawn up for all claims			
Place of search <b>THE HAGUE</b>		Date of completion of the search <b>26 January 1998</b>	Examiner <b>Diot, P</b>
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	

EPO FORM 1503 03 02 (P/04C01)



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Application Number  
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DOCUMENTS CONSIDERED TO BE RELEVANT			
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The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 26 January 1998	Examiner Diot, P
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons &amp; : member of the same patent family, corresponding document</p>			

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